

Schottky Rectifier, 400A/150V

FEATURES

- 175°C T_J operation
- Molded package
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

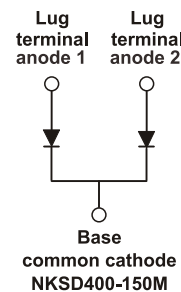
The NKSD400... Schottky rectifier common cathode module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175°C junction temperature.

TYPICAL APPLICATIONS

- High current switching power supplies
- Plating power supplies
- UPS system
- Converters
- Freewheeling
- Welder
- Reverse battery protection.



TO-244M (non-insulated)



PRODUCT SUMMARY

I _{F(AV)}	400 A
V _R	150 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNIT
I _{F(AV)}	Rectangular waveform	400	A
V _{RRM}		150	V
I _{FSM}	t _p = 5μs sine	21000	A
V _F	200 Apk, T _J = 125°C (per leg)	0.70	V
T _J	Range	-55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Maximum DC reverse voltage	V _R	150	V
Maximum working peak reverse voltage	V _{RWM}		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum average forward current per leg See fig.5	I _{F(AV)}	50% duty cycle at T _C = 129°C, rectangular waveform		200	A
				400	
Maximum peak one cycle non-repetitive surge current per leg See fig.7	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	21000	
		10 ms sine or 6 ms rect. pulse		2400	
Non- repetitive avalanche energy per leg	E _{AS}	T _J = 25°C, I _{AS} = 5.5A, L=1.0mH		15	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A =1.5V _R typical		1	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum forward voltage drop per leg See fig.1	$V_{FM}^{(1)}$	200A	$T_J = 25^{\circ}C$	0.95	V
		400A		1.20	
		200A	$T_J = 125^{\circ}C$	0.70	
		400A		0.85	
Maximum reverse leakage current per leg See fig.2	$I_{RM}^{(1)}$	$T_J = 25^{\circ}C$	$V_R = \text{Rated } V_R$	100	μA
		$T_J = 125^{\circ}C$		50	mA
Maximum junction capacitance per leg	C_T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) $25^{\circ}C$		6000	pF
Typical series inductance per leg	L_S	From top of terminal hole to mounting plane		5	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10000	V/ μs

Note

(1) Pulse width < 300 μs , duty cycle < 2%

THERMAL-MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Maximum junction and storage temperature range	T_J, T_{Stg}	-55	-	175	$^\circ\text{C}$	
Thermal resistance, junction to case per leg	TO-244M (non-insulated)	R_{thJC}	-	-	0.18	$^\circ\text{C/W}$
Thermal resistance, junction to case per module	TO-244M (non-insulated)	R_{thJC}	-	-	0.09	
Thermal resistance, case to heatsink		R_{thCS}	-	0.10	-	
Weight	TO-244M (non-insulated)		-	85 (3)	-	g(oz.)
Mounting torque ⁽¹⁾		30 (3.4)	-	40 (4.6)	lbf • in (N•m)	
Mounting torque center hole		12 (1.4)	-	18 (2.1)		
Terminal torque		30 (3.4)	-	40 (4.6)		
vertical pull		-	-	80	lbf • in	
2" lever pull		-	-	35		

Note

(1) Mounting surface must be smooth, flat, free of burrs or other protrusions.
Apply a thin even film of thermal grease to mounting surface.

Ordering Information Table

Device code						
NK	S	D	400	—	150	M
①	②	③	④		⑤	⑥

1		- NPS's power module
2		- S for Schottky Barrier Diode
3		- D for Dual Diodes, TO-244 Package
4		- Maximum average forward current, A
5		- Voltage rating (150 = 150V)
6		- "M" for molding package TO-244M

Fig.1 Maximum forward voltage drop characteristics (Per Leg)

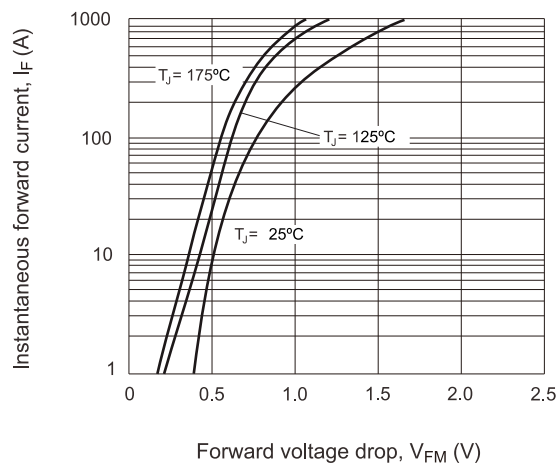
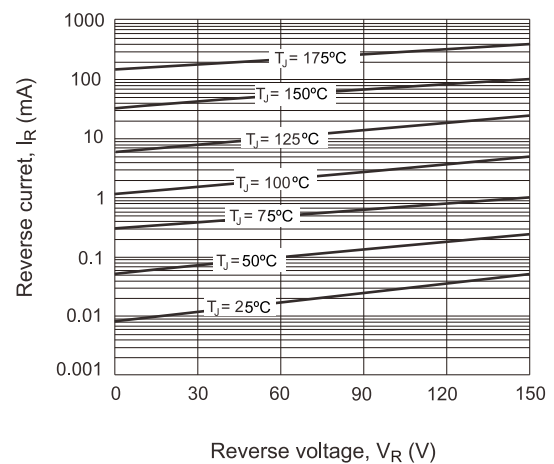


Fig.2 Typical values of reverse current vs. Reverse voltage (Per Leg)



**Fig.3 Maximum thermal impedance $R_{th(j-c)}$ characteristics
(Per Leg, for TO-244M non-insulated)**

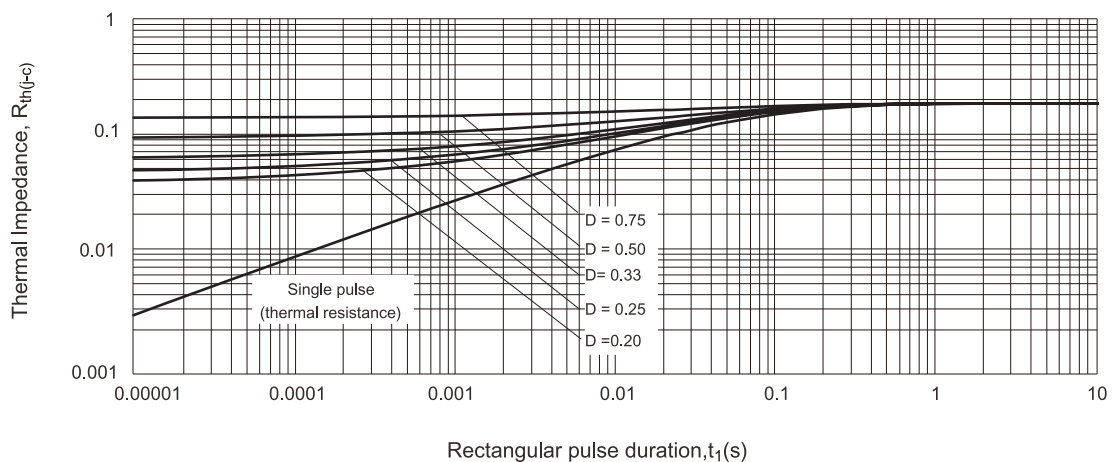


Fig.4 Typical junction capacitance vs. Reverse voltage (Per Leg)

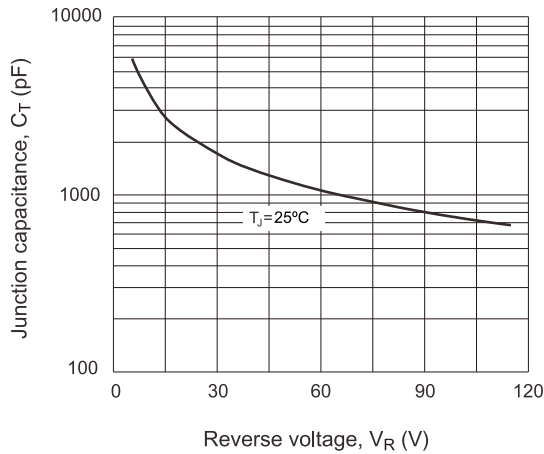


Fig.5 Maximum allowable case temperature vs. Average forward current (Per Leg)

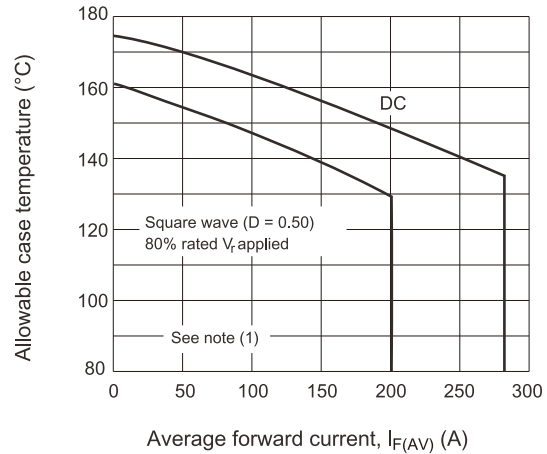


Fig.6 Forward power loss characteristics (Per Leg)

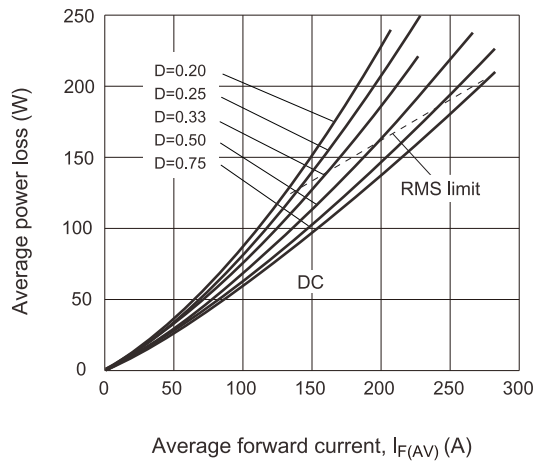


Fig.7 Maximum non-repetitive surge current (Per Leg)

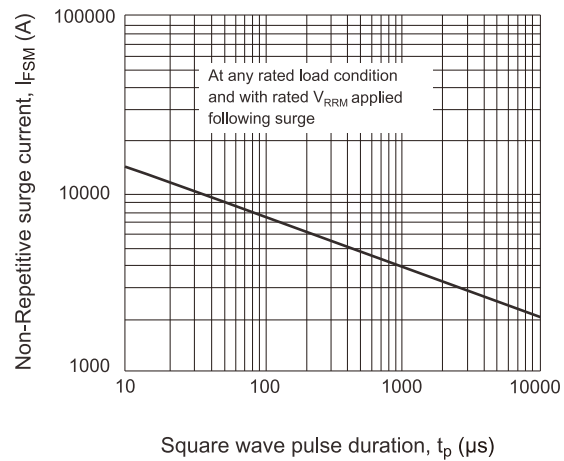
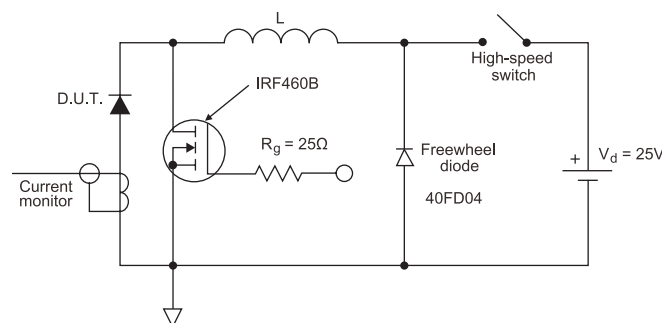


Fig.8 Unclamped Inductive test circuit



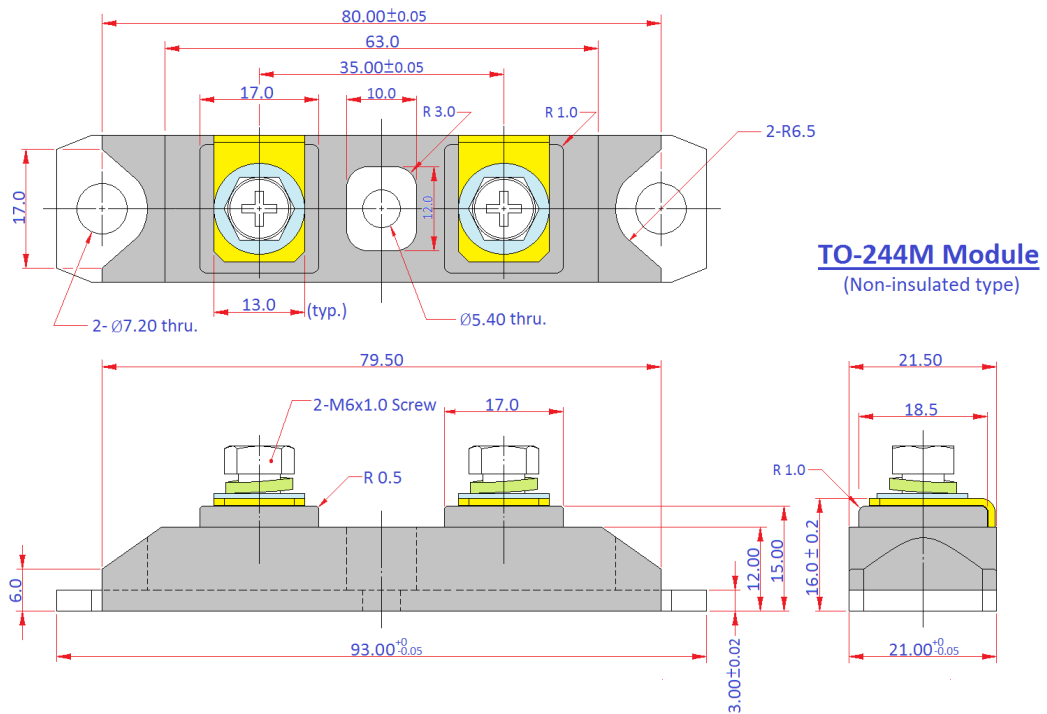
Note

(1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;

P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig.6)

$P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1-D)$; I_R at $V_{R1} = 80\%$ rated V_R

TO-244M (Non-Insulated)



All dimensions in millimeters