

Schottky Rectifier, 200A/200V

FEATURES

- 175°C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

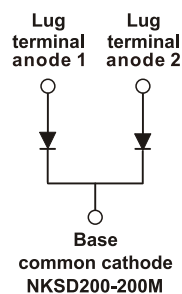
The NKSD200... Schottky rectifier common cathode module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

TYPICAL APPLICATIONS

- High current switching power supplies
- Plating power supplies
- UPS system
- Converters
- Freewheeling
- Welder
- Reverse battery protection.



TO-244M (non-insulated)



PRODUCT SUMMARY

I _{F(AV)}	200 A
V _R	200 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNIT
I _{F(AV)}	Rectangular waveform	200	A
V _{RRM}		200	V
I _{FSM}	t _p = 5 μs sine	10000	A
V _F	100 Apk, T _j = 125°C (per leg)	0.75	V
T _J	Range	-55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VALUES	UNIT
Maximum DC reverse voltage	V _R	200	V
Maximum working peak reverse voltage	V _{RWM}		

Nell High Power Products

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum average forward current See fig.5	per leg per device	I _{F(AV)}	50% duty cycle at T _C = 125°C, rectangular waveform		100	A
					200	
Maximum peak one cycle non-repetitive surge current per leg See fig.7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	10000	
			10 ms sine or 6 ms rect. pulse		1200	
Non- repetitive avalanche energy per leg		E _{AS}	T _J =25°C, I _{AS} =5.5A, L=1.0mH		15	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A =1.5xV _R typical		1	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum forward voltage drop per leg See fig.1	$V_{FM(1)}$	100A	$T_J = 25^{\circ}\text{C}$	1.0	V
		200A		1.3	
		100A	$T_J = 125^{\circ}\text{C}$	0.75	
		200A		0.85	
Maximum reverse leakage current per leg See fig.2	$I_{RM(1)}$	$T_J = 25^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	50	μA
		$T_J = 125^{\circ}\text{C}$		30	mA
Maximum junction capacitance per leg	C_T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25°C		3000	pF
Typical series inductance per leg	L_S	From top of terminal hole to mounting plane		7	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10000	V/ μs

Note

(1) Pulse width < 300 μs , duty cycle < 2%

THERMAL-MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction and storage temperature range		T _J , T _{Stg}	-55	-	175	°C
Thermal resistance, junction to case per leg	TO-244M (non-insulated)	R _{thJC}	-	-	0.32	°C/W
Thermal resistance, junction to case per module	TO-244M (non-insulated)	R _{thJC}	-	-	0.16	
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
Weight	TO-244M (non-insulated)		-	85 (3)	-	g(oz.)
Mounting torque			35.4 (4)	-	53.1 (6)	lbf • in (N•m)
Mounting torque center hole			30 (3.4)	-	40 (4.6)	
Terminal torque			30 (3.4)	-	44.2 (5)	
vertical pull			-	-	80	lbf • in
2" lever pull			-	-	35	

Ordering Information Table

Device code	NK	S	D	200	—	200	M
	1	2	3	4		5	6
	1	2	3	4		5	6
	1	2	3	4		5	6
	1	2	3	4		5	6
	1	2	3	4		5	6
	1	2	3	4		5	6

- 1 - Nell's power module
- 2 - S for Schottky Barrier Diode
- 3 - D for Dual Diodes, TO-244M Package
- 4 - Maximum average forward current, A
- 5 - Voltage rating (200 = 200V)
- 6 - "M" for molding package , TO-244M

Fig.1 Maximum forward voltage drop characteristics (Per Leg)

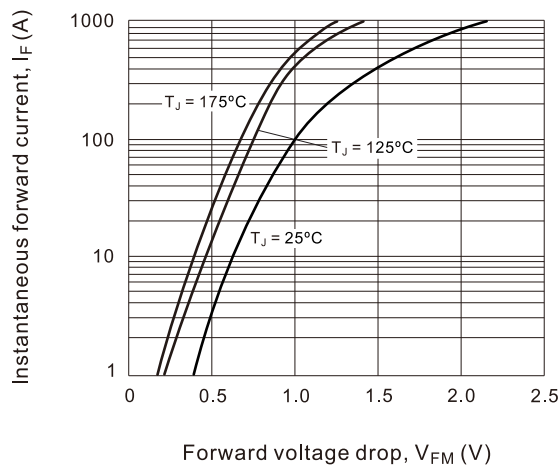


Fig.2 Maximum values of reverse current vs. Reverse voltage (Per Leg)

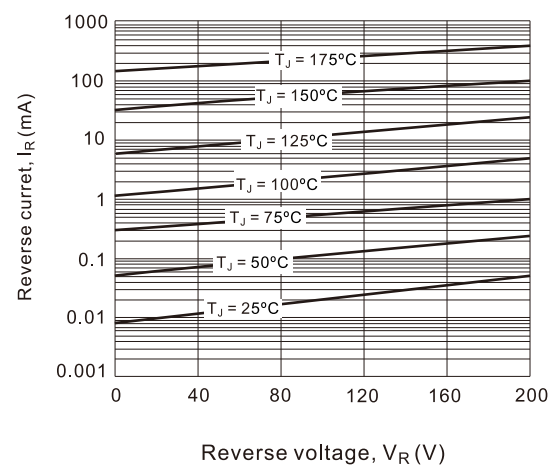


Fig.3 Maximum thermal impedance $R_{th(j-c)}$ characteristics (Per Leg, for TO-244M non-insulated)

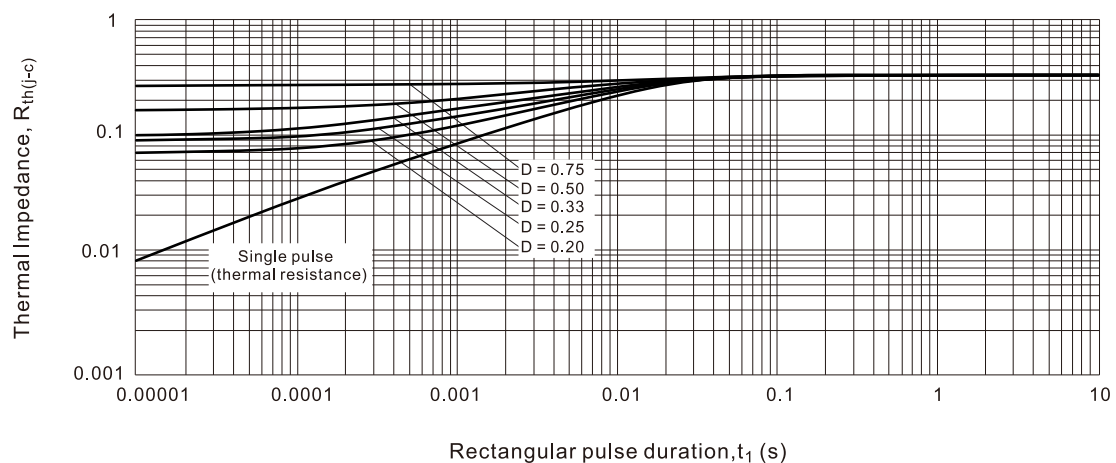


Fig.4 Typical junction capacitance vs. Reverse voltage (Per Leg)

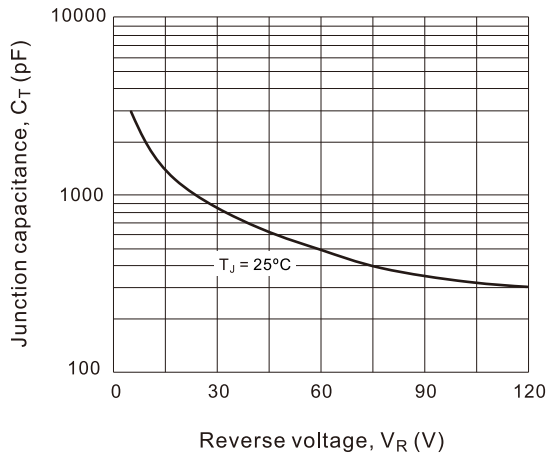


Fig.5 Maximum allowable case temperature vs. Average forward current (Per Leg)

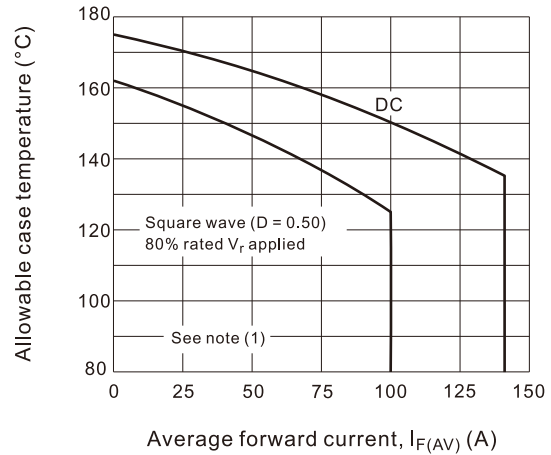


Fig.6 Forward power loss characteristics (Per Leg)

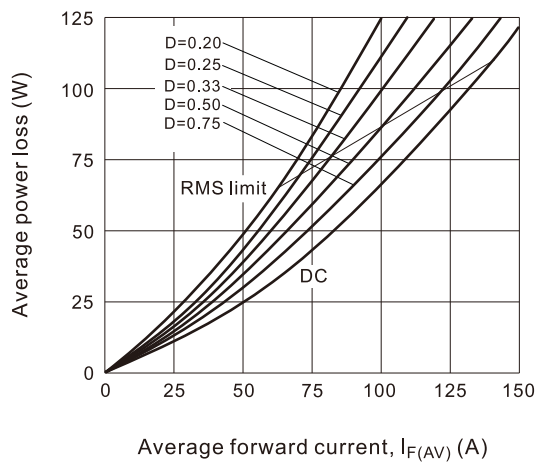


Fig.7 Maximum non-repetitive surge current (Per Leg)

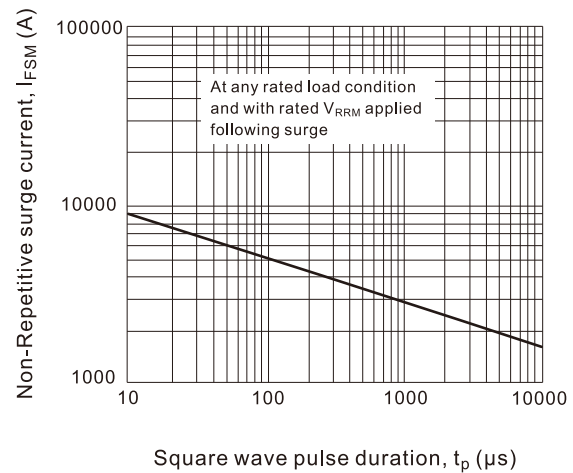
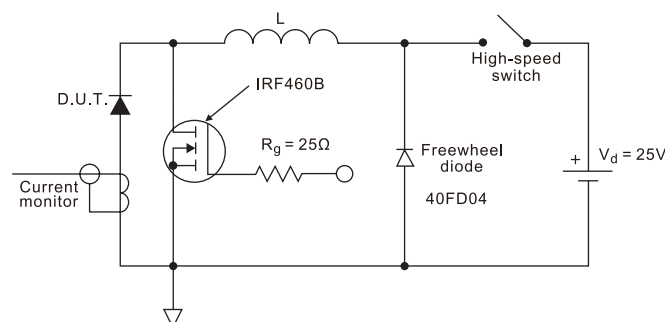


Fig.8 Unclamped Inductive test circuit



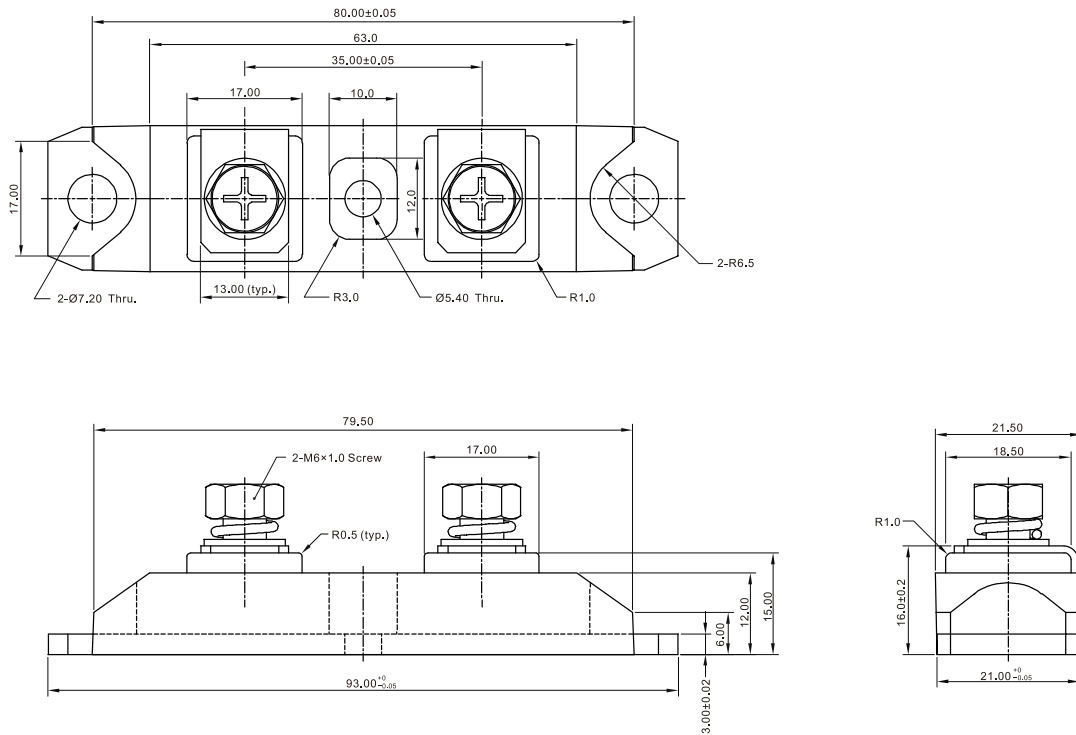
Note

(1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;

P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig.6)

P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1-D)$; I_R at $V_{R1} = 80\%$ rated V_R

TO-244M (Non-Insulated)



All dimensions in millimeters