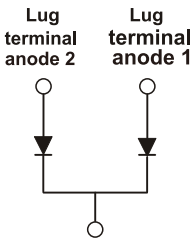
FRED
Ultrafast Soft Recovery Diode Module
150A x 2 / 600V

FEATURES

- Very low Q_{rr} and t_{rr}
- Lead (Pb)-free
- Designed and qualified for industrial level
- Reduced RFI and EMI
- Reduced snubbing
- Planar FRED Chip
- Epoxy molding structure
- 175°C Junction temperature

TO-244M (non-insulated)



DESCRIPTION

FRED diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems .
An extensive characterization of the recovery behavior for different values of current, temperature and dI/dt simplifies the calculations of losses in the operating conditions.The softness of the recovery eliminates the need for a snubber in most applications.

TYPICAL APPLICATIONS

- Power converters
- Motor drives
- Welders
- Switching power supplies
- Uninterruptible power supply (UPS)
- Power factor correction (PFC) circuit
- Inverter
- Choppers
- Battery chargers

PRODUCT SUMMARY	
$I_{F(AV)}$	300A
V_R	600V
$I_{F(DC)}$ at T_C	185A at 100 °C

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		MAX.	UNIT
Cathode to anode voltage	V_R			600	V
Average forward current	$I_{F(AV)}$	T_C =25°C, per leg		345	A
		T_C =115°C	per device	300	
			per leg	150	
Single pulse forward current	I_{FSM}	Limited by junction temperature, per leg		1800	
Non-repetitive avalanche energy	E_{AS}	L = 100 µH, duty cycle limited by maximum T_J		2.2	mJ
Maximum power dissipation	P_D	T_C =25°C		465	W
		T_C =100°C		205	
Operating junction and storage temperature range	T_J, T_{Stg}			- 55 to 175	°C

ELECTRICAL SPECIFICATIONS (T_J = 25 ºC unless otherwise specified)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Cathode to anode breakdown voltage	V_{BR}	I_R = 100 μA	600	-	-	V
Maximum forward voltage	V_{FM}	I_F = 150 A	-	1.20	1.35	
		I_F = 300 A	-	1.35	1.50	
		I_F = 150 A, T_J = 125 ºC	-	1.0	1.15	
Maximum reverse leakage current per leg	I_{RM}	T_J = 125°C, V_R = 600V	-	1.0	4	mA
		T_J = 25°C, V_R = 600V	-	2.0	10	μA
Junction capacitance	C_T	V_R = 200V	-	300	450	pF
Series inductance	L_S	From top of terminal hole to mounting plane	-	6.0	-	nH

DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T_J = 25 ºC unless otherwise specified)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Reverse recovery time	t_{rr}	I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A		-	105	120	ns
		I_F = 1.0A, dI_F/dt=200A/μs, V_R = 30V		-	75	-	
		T_J = 25 ºC	I_F = 150A dI_F/dt = 200 A/μs V_R = 200 V	-	95	150	
		T_J = 125 ºC		-	230	350	
Peak recovery current	I_{RRM}	T_J = 25 ºC		-	11	21	A
		T_J = 125 ºC		-	17	30	
Reverse recovery charge	Q_{rr}	T_J = 25 ºC		-	600	1800	nC
		T_J = 125 ºC		-	1350	4100	
Peak rate of recovery current	$dI_{(rec)M}/dt$	T_J = 25 ºC		-	290	-	A/μs
		T_J = 125 ºC		-	250	-	

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Maximum junction and storage temperature range	T_J, T_{stg}	-55	-	175	ºC
Thermal resistance, junction to case per leg	R_{thJC}	-	-	0.22	ºC/W
Thermal resistance, junction to case per module	R_{thJC}	-	-	0.11	
Typical thermal resistance, case to heatsink	R_{thCS}	-	0.10	-	
Weight		-	80 (2.82)	-	g (oz.)
Mounting torque(1)		30 (3.4)	-	40 (4.6)	lbf·in (N·m)
Mounting torque center hole		12 (1.4)	-	18 (2.1)	
Terminal torque		30 (3.4)	-	40 (4.6)	
Vertical pull		-	-	80	lbf·in
2″ lever pull		-	-	35	

Note

(1)Mounting surface must be smooth, flat, free of burrs or other protrusions. Apply a thin even film or thermal grease to mounting surface. Gradually tighten each mounting bolt in 5 to 10 lbf. in steps until desired or maximum torque limits are reached.

Ordering Information Table

Device code

NK	F	D	300	-	60	M
1	2	3	4		5	6

1 - Nell's power module
2 - F for Ultrafast soft recovery diode
3 - D for Dual Diodes, TO-244 Package
4 - Maximum average forward current, 300 = 150A x 2
5 - Voltage rating (60 = 600V)
6 - M for Molding structure

Fig.1 Typical forward voltage drop vs. Instantaneous forward current (per leg)

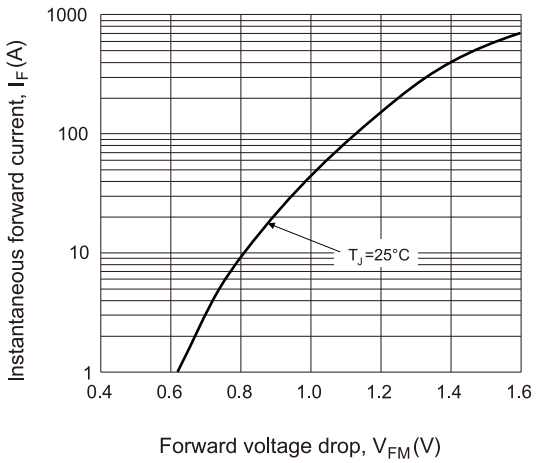


Fig.2 Typical reverse current vs. reverse voltage (per leg)

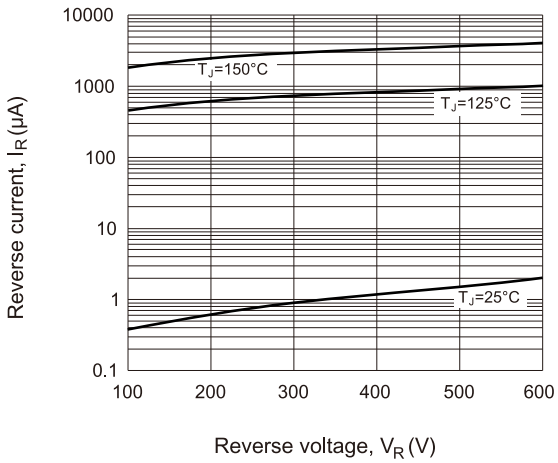


Fig.3 Typical junction capacitance vs. reverse voltage (per leg)

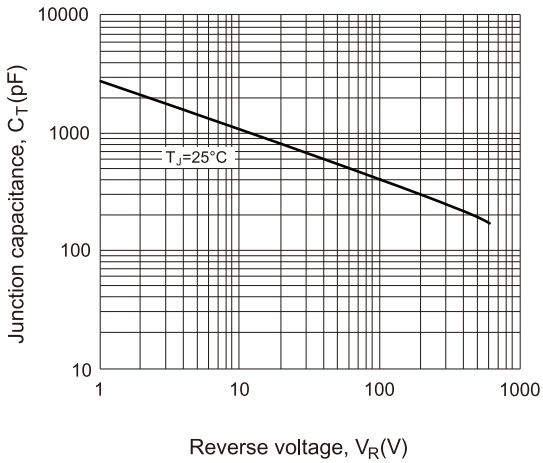


Fig.4 Maximum allowable case temperature vs. DC forward current (per leg)

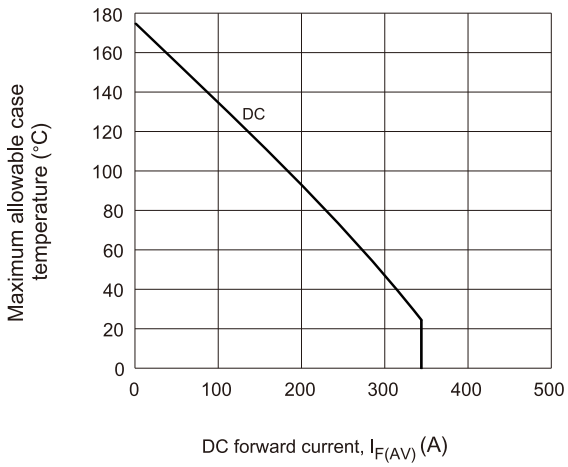


Fig.5 Maximum thermal impedance $R_{th(j-c)}$ characteristics (per leg, for TO-244M non-insulated)

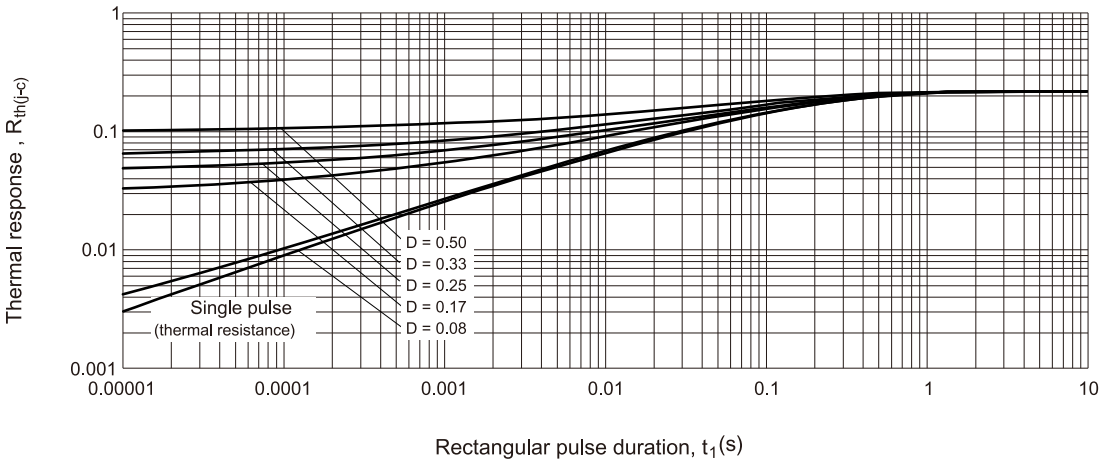


Fig.6 Reverse recovery parameter test circuit

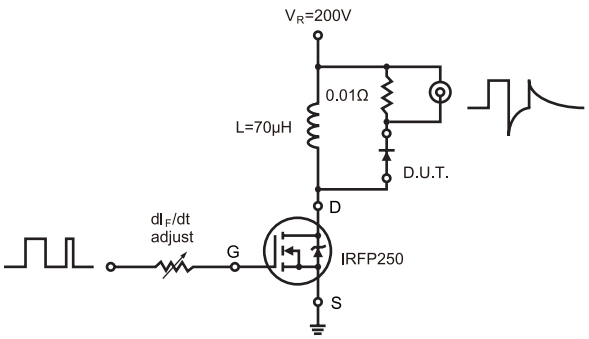
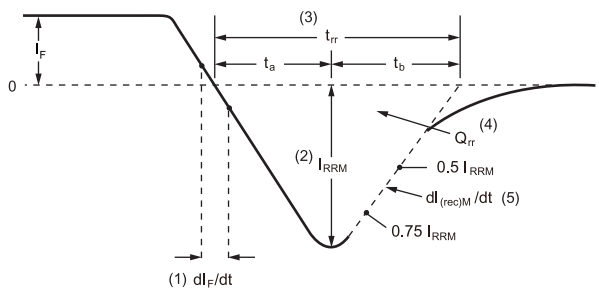


Fig.7 Reverse recovery waveform and definitions



(1) dI_F/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

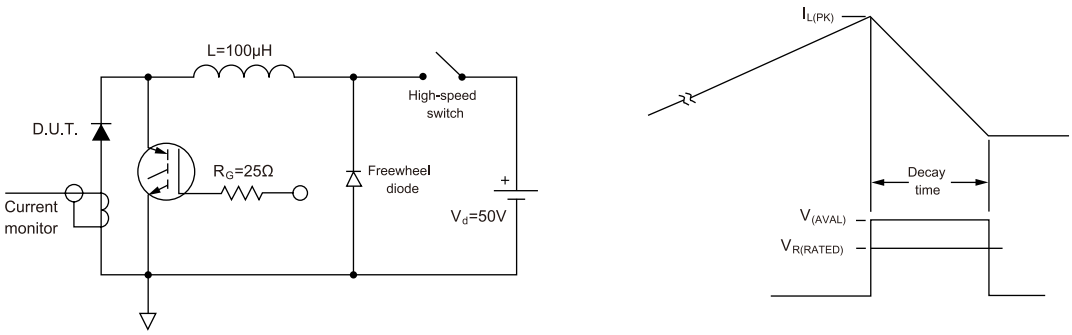
(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

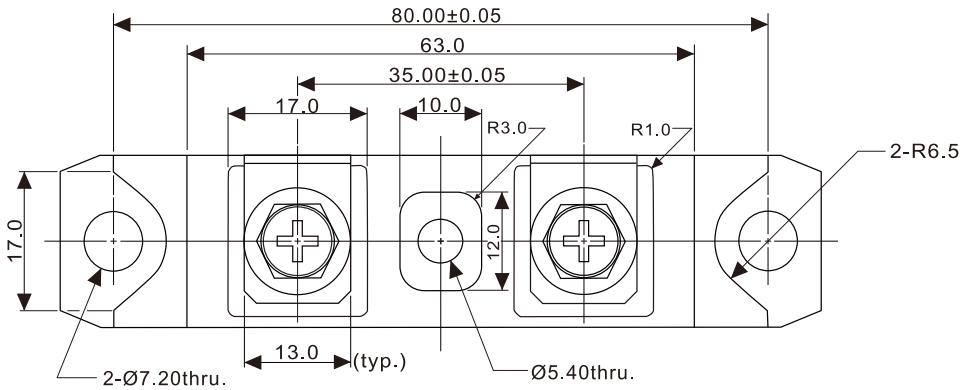
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

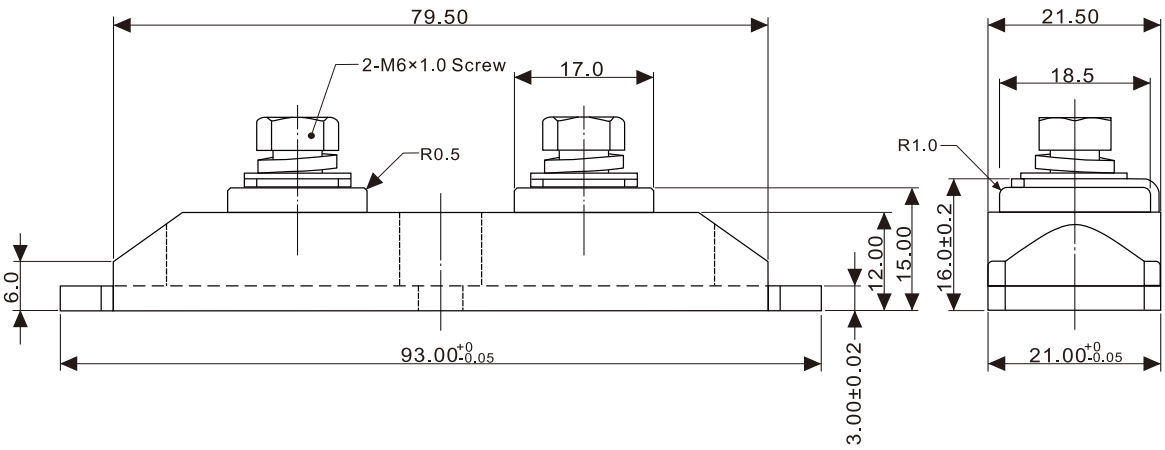
(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig.8 Avalanche test circuit and waveforms



TO-244M (Non-Insulated)





All dimensions in millimeters